

**[General Information]**

<b>Processor Name:</b>	<b>Intel Pentium-M-1400</b>	<b>Intel Pentium-M 745</b>
<b>Original Processor Frequency:</b>	<b>1400.0 MHz</b>	<b>1800.0 MHz</b>
<b>Original Processor Frequency [MHz]:</b>	<b>1400</b>	<b>1800</b>
CPU ID:	695	000006D6
<b>CPU Brand Name:</b>	<b>Intel(R) Pentium(R) M processor 1400MHz</b>	<b>Intel(R) Pentium(R) M processor 1.80GHz</b>
CPU Vendor:	GenuineIntel	GenuineIntel
CPU Stepping:	B1	B1
<b>CPU Code Name:</b>	<b>Banias</b>	<b>Dothan</b>
<b>CPU Technology:</b>	<b>130 nm</b>	<b>90 nm</b>
<b>CPU S-Spec:</b>	<b>SL6F8, SL6F5</b>	<b>Q68Q, Q95Q, SL7Q6, Q843, QABA, Q58Q, Q65T, Q47T, Q85Q, Q56Q, Q45T, Q99Z, Q57T, SL7EN, SL7EQ</b>
<b>CPU Thermal Design Power (TDP):</b>	<b>22 W</b>	<b>21 W</b>
CPU Type:	Production Unit	
CPU Platform:	mPGA479M (Micro-FCPGA/FCBGA)	mPGA479M (Micro-FCPGA/FCBGA)
<b>Microcode Update Revision:</b>	<b>7</b>	<b>18</b>
Number of CPU Cores:	1	1
Number of Logical CPUs:	1	1

**[Operating Points]**

<b>CPU LFM (Minimum):</b>	<b>600.0 MHz = 6 x 100.0 MHz @ 0.9560 V</b>	<b>600.0 MHz = 6 x 100.0 MHz @ 0.9880 V</b>
<b>CPU HFM (Base):</b>	<b>1400.0 MHz = 14 x 100.0 MHz @ 1.4840 V</b>	<b>1800.0 MHz = 18 x 100.0 MHz @ 1.3400 V</b>
<b>CPU Current:</b>	<b>1395.4 MHz = 14 x 99.7 MHz @ 1.4840 V</b>	<b>1794.2 MHz = 18 x 99.7 MHz @ 1.3400 V</b>
<b>CPU Bus Type:</b>	<b>FSB</b>	<b>FSB (QDR)</b>

**[Cache and TLB]**

<b>L1 Cache:</b>	<b>Instruction: 32 KBytes, Data: 32 KBytes</b>	<b>Instruction: 32 KBytes, Data: 32 KBytes</b>
<b>L2 Cache:</b>	<b>Integrated: 1 MBytes</b>	<b>Integrated: 2 MBytes</b>
Instruction TLB:	4 MB Pages, Fully associative, 2 entries	4 MB Pages, Fully associative, 2 entries
Data TLB:	4 MB Pages, 4-way set associative, 8 entries	4 MB Pages, 4-way set associative, 8 entries

**[Standard Feature Flags]**

FPU on Chip	Present	Present
Enhanced Virtual-86 Mode	Present	Present
I/O Breakpoints	Present	Present
Page Size Extensions	Present	Present
Time Stamp Counter	Present	Present
Pentium-style Model Specific Registers	Present	Present
Physical Address Extension	Not Present	Not Present
Machine Check Exception	Present	Present
CMPXCHG8B Instruction	Present	Present
APIC On Chip / PGE (AMD)	Not Present	Not Present
Fast System Call	Present	Present
Memory Type Range Registers	Present	Present
Page Global Feature	Present	Present
Machine Check Architecture	Present	Present
CMOV Instruction	Present	Present
Page Attribute Table	Present	Present
36-bit Page Size Extensions	Not Present	Not Present
Processor Number	Not Present	Not Present
CLFLUSH Instruction	Present	Present
Debug Trace and EMON Store	Present	Present
Internal ACPI Support	Present	Present
MMX Technology	Present	Present
Fast FP Save/Restore (IA MMX-2)	Present	Present
Streaming SIMD Extensions	Present	Present
Streaming SIMD Extensions 2	Present	Present
<b>Self-Snoop</b>	<b>Not Present</b>	<b>Present</b>
Multi-Threading Capable	Not Present	Not Present
Automatic Clock Control	Present	Present
IA-64 Processor	Not Present	Not Present
Signal Break on FERR	Present	Present
Virtual Machine Extensions (VMX)	Not Present	Not Present
Safer Mode Extensions (Intel TXT)	Not Present	Not Present
Streaming SIMD Extensions 3	Not Present	Not Present
Supplemental Streaming SIMD Extensions 3	Not Present	Not Present
Streaming SIMD Extensions 4.1	Not Present	Not Present
Streaming SIMD Extensions 4.2	Not Present	Not Present
AVX Support	Not Present	Not Present
Fused Multiply Add (FMA)	Not Present	Not Present

Carryless Multiplication (PCLMULQDQ)/GFMUL	Not Present	Not Present
CMPXCHG16B Support	Not Present	Not Present
MOVBE Instruction	Not Present	Not Present
POPCNT Instruction	Not Present	Not Present
XSAVE/XRSTOR/XSETBV/XGETBV Instructions	Not Present	Not Present
XGETBV/XSETBV OS Enabled	Not Present	Not Present
Float16 Instructions	Not Present	Not Present
AES Cryptography Support	Not Present	Not Present
Random Number Read Instruction (RDRAND)	Not Present	Not Present
Extended xAPIC	Not Present	Not Present
MONITOR/MWAIT Support	Not Present	Not Present
Thermal Monitor 2	Present	Present
Enhanced SpeedStep Technology	Present	Present
L1 Context ID	Not Present	Not Present
Send Task Priority Messages Disabling	Not Present	Not Present
Processor Context ID	Not Present	Not Present
Direct Cache Access	Not Present	Not Present
TSC-deadline Timer	Not Present	Not Present
Performance/Debug Capability MSR	Not Present	Not Present
IA32 Debug Interface Support	Not Present	Not Present
64-Bit Debug Store	Not Present	Not Present
CPL Qualified Debug Store	Not Present	Not Present

**[Extended Feature Flags]**

64-bit Extensions	Not Present	Not Present
RDTSCP and TSC_AUX Support	Not Present	Not Present
1 GB large page support	Not Present	Not Present
No Execute	Not Present	Not Present
SYSCALL/SYSRET Support	Not Present	Not Present

**[Enhanced Features]**

Thermal Monitor 1:	Supported, Enabled	Supported, Enabled
Thermal Monitor 2:	Supported, Disabled	Supported, Disabled
Enhanced Intel SpeedStep (GV3):	Supported, Enabled	Supported, Enabled
Bi-directional PROCHOT#:	N/A	N/A
Extended Auto-HALT State C1E:	N/A	N/A
MLC Streamer Prefetcher	Not Supported	Not Supported
MLC Spatial Prefetcher	Not Supported	Not Supported
DCU Streamer Prefetcher	Not Supported	Not Supported
DCU IP Prefetcher	Not Supported	Not Supported
Intel Dynamic Acceleration (IDA) Technology:	Not Supported	Not Supported
Intel Dynamic FSB Switching:	Not Supported	Not Supported
Intel Turbo Boost Technology:	Not Supported	Not Supported
Programmable Ratio Limits:	Not Supported	Not Supported
Programmable TDC/TDP Limits:	Not Supported	Not Supported
Hardware Duty Cycling:	Not Supported	Not Supported

**[Memory Ranges]**

Maximum Physical Address Size:	32-bit (4 GBytes)	32-bit (4 GBytes)
Maximum Virtual Address Size:	32-bit (4 GBytes)	32-bit (4 GBytes)

**[MTRRs]**

Range 0-80000000 (0MB-2048MB) Type:	Write Back (WB)	Write Back (WB)
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